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Abstract of the Disclosure

If a re-instruction fetch request is inputted to a box 30, a signal for holding writing in a branch history or a signal for holding a re-instruction fetch is generated. A signal for holding the re-instruction branch history information, fetch, branch a branch instruction and address destination a instruction address are inputted to the box 31. The 31 outputs these data, and simultaneously box generates a new entry in the branch history, updates an old entry or generates a signal for instructing to erase an entry. These data are inputted to the box 32, and a signal for instructing whether to write a branch instruction address, a branch destination instruction address or branch history information are generated.